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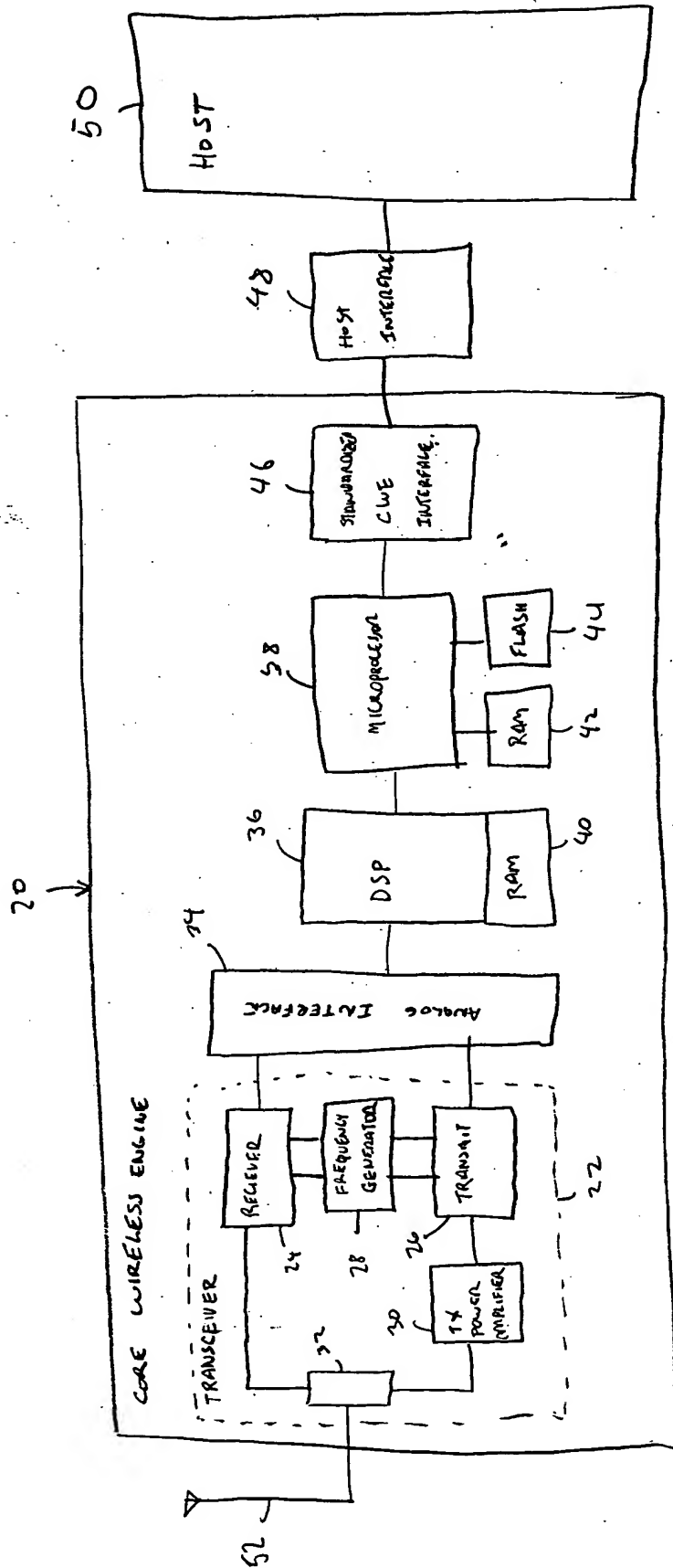


FIGURE 1

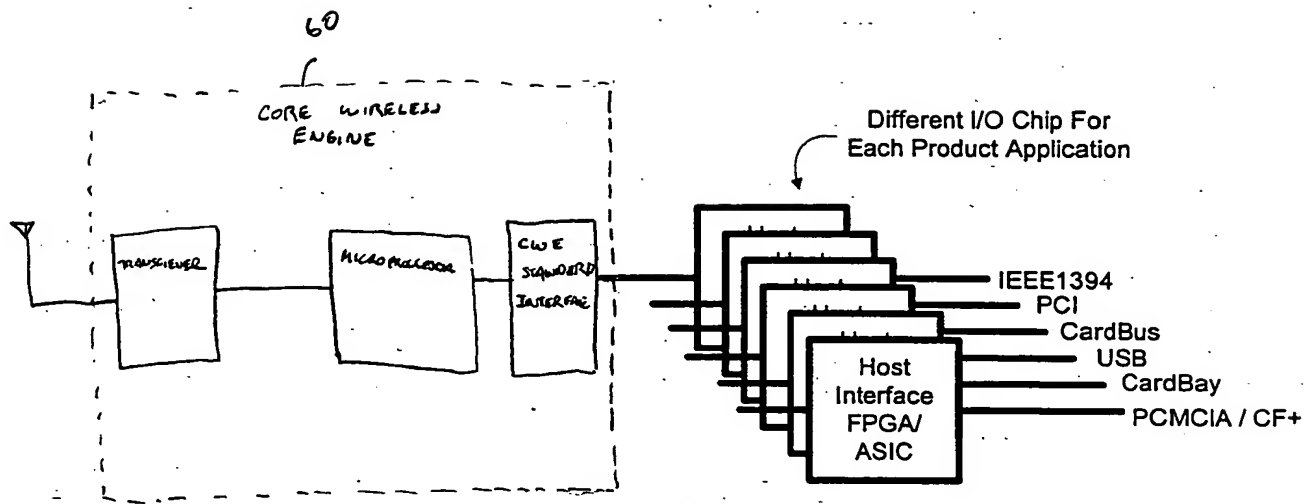


FIGURE 2

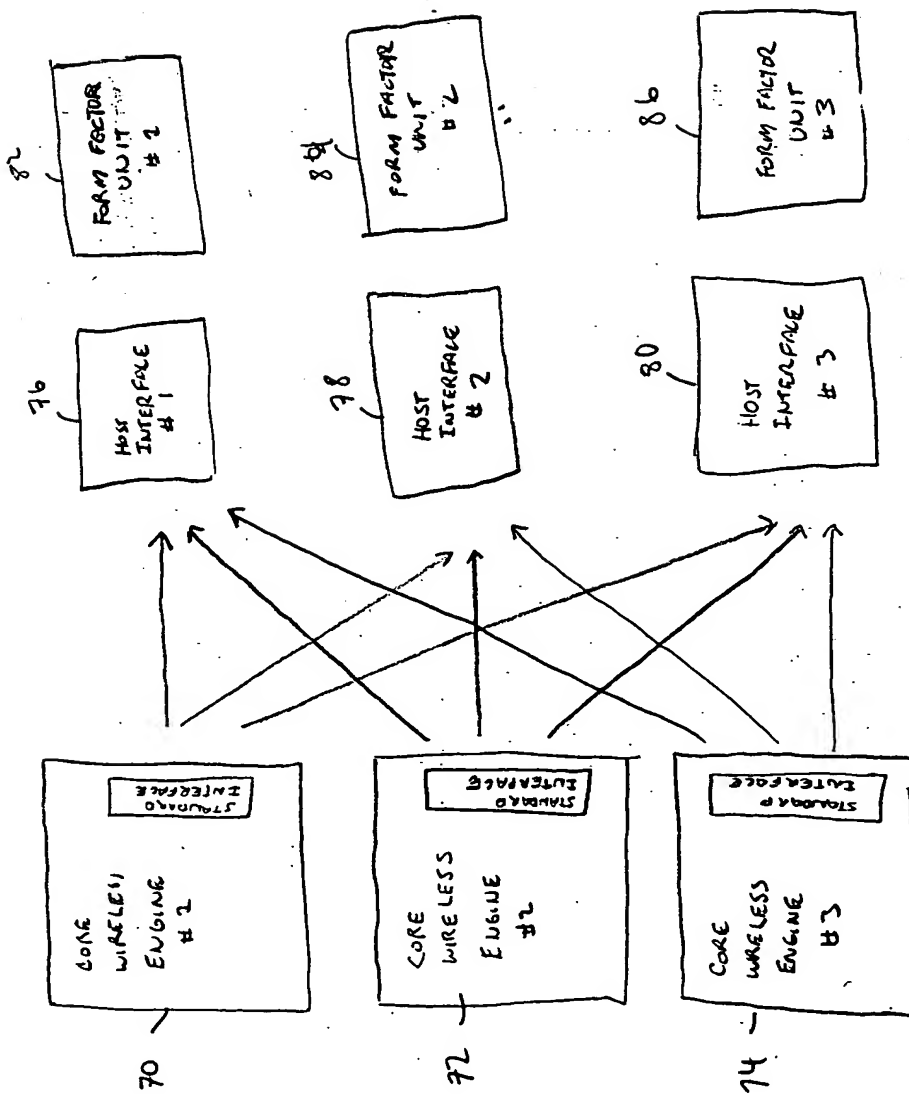


FIGURE 3

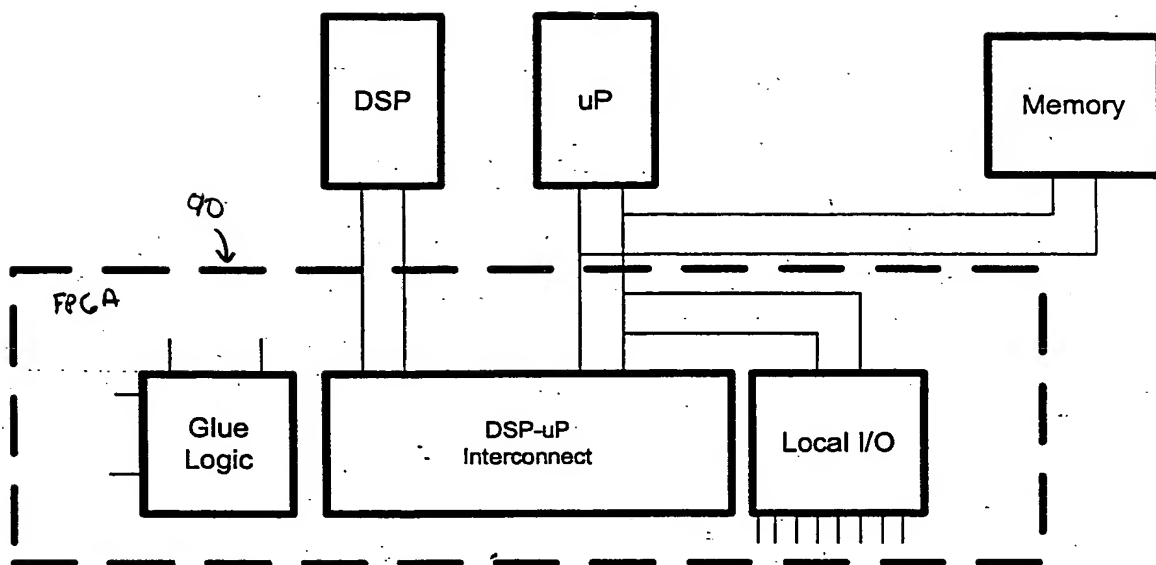


FIGURE 4A

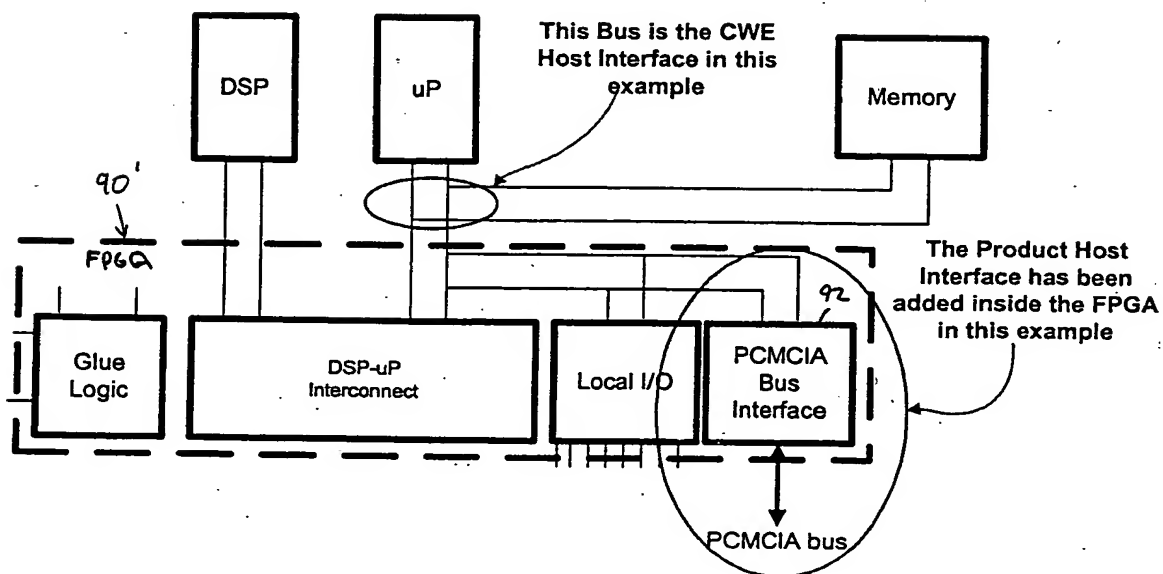


FIGURE 4B

# Registers Visible to CWE MCU across CWE Host Interface

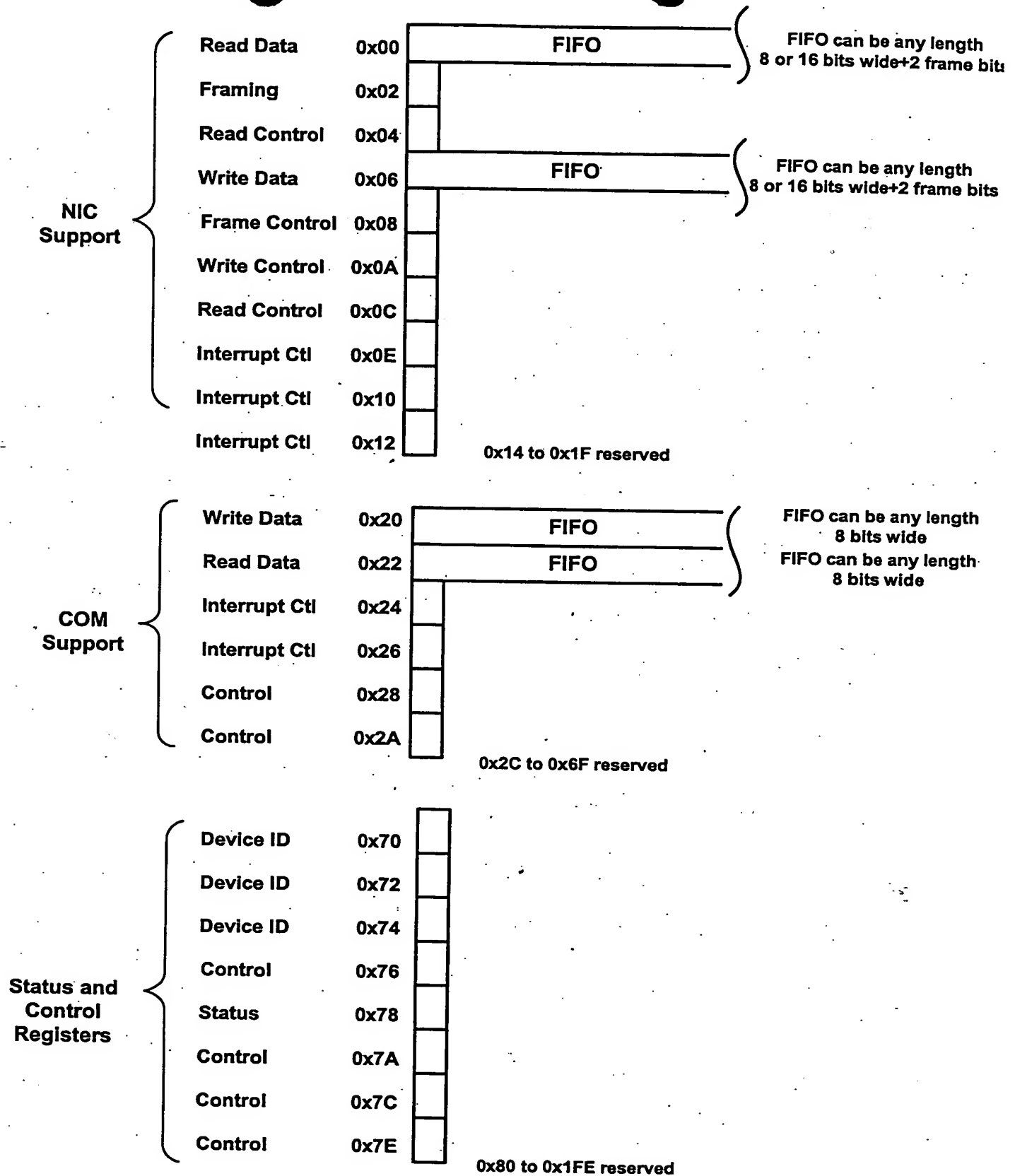


FIGURE 5

## CWE Host Interface

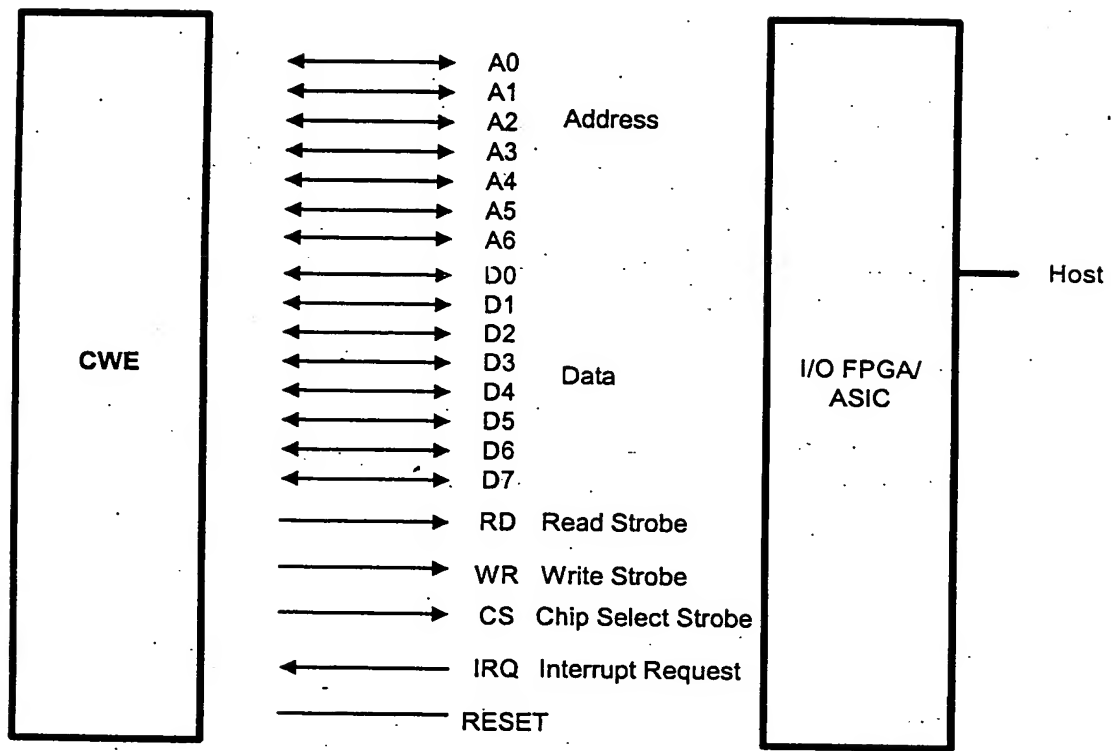


FIGURE 6

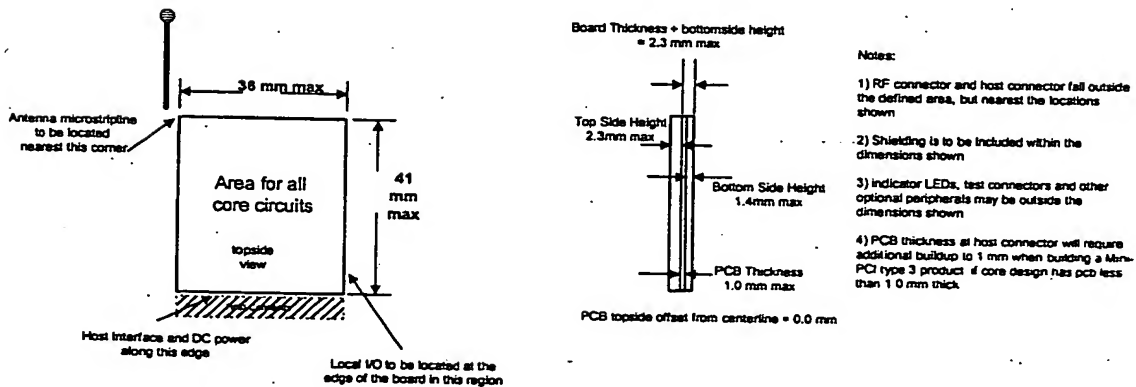


FIGURE 7

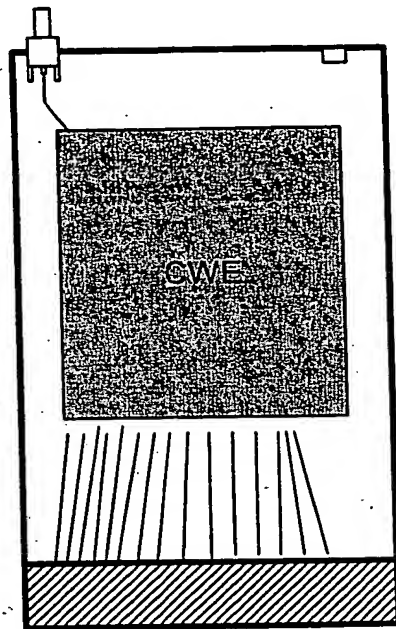


FIGURE 8A

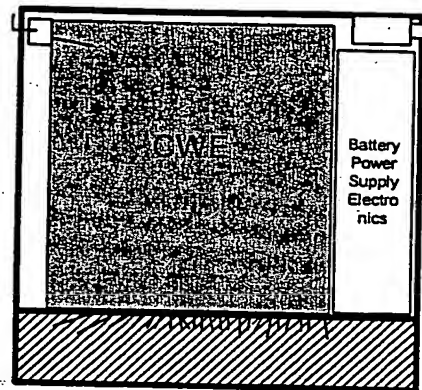


FIGURE 8B

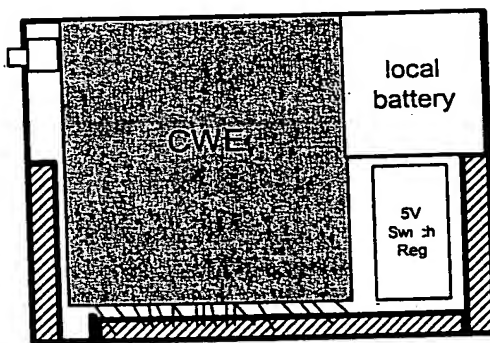


FIGURE 8C

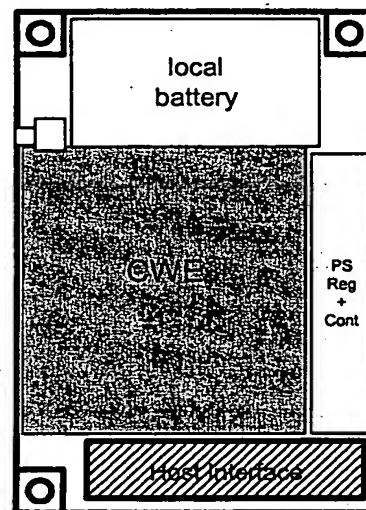


FIGURE 8D



FIGURE 8E

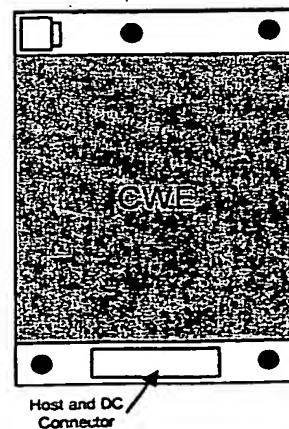


FIGURE 8F

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